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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,327	03/30/2004	Woon-Sik Suh	8729-231 (SS-20446-US)	5102
22150	7590	07/27/2006	EXAMINER	
F. CHAU & ASSOCIATES, LLC 130 WOODBURY ROAD WOODBURY, NY 11797			WENDELL, ANDREW	
			ART UNIT	PAPER NUMBER
			2618	

DATE MAILED: 07/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief	Application No.	Applicant(s)	
	10/813,327	SUH ET AL.	
	Examiner	Art Unit	
	Andrew Wendell	2618	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 17 July 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☐ The period for reply expires _____ months from the mailing date of the final rejection.
b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
- (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
(b) ☐ They raise the issue of new matter (see NOTE below);
(c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
(d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. ☐ Applicant's reply has overcome the following rejection(s): _____.
6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
The status of the claim(s) is (or will be) as follows:
Claim(s) allowed: _____.
Claim(s) objected to: _____.
Claim(s) rejected: 1-48.
Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). _____.
13. ☐ Other: _____.

Continuation of 11. does NOT place the application in condition for allowance because: Regarding claims 1, 11, 21, and 31, applicant remarks "Therefore, Dias does not teach a processor bus having address lines and M data lines being reduced or "packed" down into a packet bus (nor any other sort of bus) having fewer (N) "data lines"." Examiner's response is Dias does teach N data lines because lines 1.22 and 1.26 are control data (Col. 8 lines 42-45) for the control logic 1.30 (Fig. 1.2). Dias further teaches a plurality of M data lines (1.17, Col. 8 lines 34-38). The combination including Funk et al. teaches the data lines being packetized ("data blocks," Col. 5 lines 16-26 and Col. 7 lines 15-40). Applicant remarks "Furthermore, the security circuit 1.10 (Fig. 1.2) requiring only one single "data line" selected from a standard "parallel port" interface, does not teach a high-data-volume peripheral such as a "modem", and thus it does not provide any suggestion or motivation to combine the features of Dias with the circuit of Funk." Examiner's response is the Dias et al. reference was used to show that M lines are greater than N data lines. Both Dias et al. and Funk et al. circuit's have a logic unit and a central processing unit with data lines. Further, "high-data-volume peripheral" is not a limitation in claims 1, 11, 21, and 31. Applicant remarks "Funk does not teach or suggest sending a "packetized command" over the N "data" lines of the Processor-Modem bus ("430, 432")." Examiner's response is Funk does teach control data ("control information," Col. 3 lines 26-30). Also, Funk does teach packetized data ("data blocks," Col. 5 lines 16-26 and Col. 7 lines 15-40). Applicant remarks "The various characterizations of the bus(es) 430, 432, and 434 (shown in Fig. 4 of Funk) by the Examiner are internally inconsistent." Examiner's response is claims 1, 11, 21, and 31 were amended and therefore changed the scope of the claims from the non-final issued previously. Also, the claim is broad enough to read different interpretations on the limitations. Applicant remarks "Therefore, Funk does not teach "a packet bus" (distinct from the "processor bus") between the processor (421) and the modem (101), as claimed in claims 1, 11, and 31." Examiner's response is see Col. 5 lines 16-26 and Col. 7 lines 15-40. Applicant remarks "Applicant believes that "operatively connected" is synonymous with "on the bus"." Examiner's response is "operatively" is a broad term and the rejection made reads on for the word operatively. As applicant points out "operatively" has many different meanings and "operatively" is not defined in the claim to mean "on the bus" specifically.

Andrew Wendell

571-272-0557

Quochien B. Vuong 7/24/06

QUOCHIEN B. VUONG
PRIMARY EXAMINER